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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

### Complete if Known

Application Number	10/675,831
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Dillon J. Cody
Art Unit	2183
Attorney Docket No.	AUS920030479US1

[illegible]

# FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

<b>Examiner Initial</b>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

Examiner: /Dillon Cody/ (05/17/2006)

**Date Considered:** 05/17/2006

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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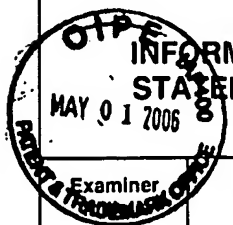
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<b>Examiner Initial</b>		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
<b>DC</b>	<b>AB</b>	<b>Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.</b>

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DC	AA 5,710,881	01-20-1998	Gupta et al.	
	AB 5,822,763	10-13-1998	Baylor et al.	
	AC 5,928,334	07-27-1999	Mandym et al.	
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	AM 6,925,424 B2	08-02-2005	Jones et al.	

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DC	AN TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
	AO TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
	AP ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
	AQ Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
	AR "Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
	AS HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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